

ABOUT THE INSTITUTION

The Kalasalingam Institute of Technology (KIT) was established in the year 2008 by our **Chairman late Kalvivalal Thiru. T.Kalasalingam**, run by “Kalasalingam and Anandam Ammal Charities”. Now, this esteemed institution is functioning under leadership of **Ilayavallal Dr.K.Sridharan,MBA.,Ph.D., Secretary, Dr.S.Arivazhagi,M.B.B.S.,Director, Dr.S.ShasiAnand,Ph.D., Director, Mr.S.Arjun Kalasalingam,MS., Director and Dr.S.Harikrishnan, M.E.,Ph.D.,Principal.**

KIT is located in a sprawling area of 25 acres in Anand nagar, Krishnankoil. The Institution is approved by Govt. of TamilNadu and AICTE, New Delhi. It is affiliated to Anna University, Chennai. The college is offering the UG courses such as **ECE, CIVIL, EEE, CSE, IT** and PG courses such as **M.E in VLSI & CSE**. Our college is one of the **NPTEL Nodal center** which provides video lectures and power point presentations for nearly 850 courses to enhance the teaching & learning process. The Institution has strong Industrial Interaction Cell and 25 faculty numbers are pursuing Ph.D programme from different domain in various Universities. Our recruiters are Infosys, Wipro, HCL, Infoview, Vernalis, Tessolve, Zoho, Sutherland, etc

ABOUT THE DEPARTMENT

The department of ECE is offering **B.E in ECE** programme with an intake of **120 seats** and **M.E in VLSI Design** programme. Since the Kalasalingam Institute of Technology was established in the year 2008, with a view to offer quality in higher education to numerous young fellows. The programme follows the course curriculum given by Anna University, Chennai.

ABOUT THE FDTP

The objective of the FDTP is to bring together experts from Industries, Academic and other user communities on a common platform to discuss and share their expertise and ideas about the technologies & recent trends in the field of Linear Integrated circuits (LIC).

COURSE COVERAGE

The main focus will be on Anna University (Regulation 2017), B.E-ECE 4thsemester syllabus of EC8453 – LIC.

The following topics will be covered:

- Basics of Operational Amplifiers
- Applications of Operational Amplifiers
- Analog Multiplier and PLL
- Application of PLL for AM and FM detection
- Analog to Digital & Digital to Analog converters
- Waveform generators and Special function ICs

ABOUT REGISTRATION

- Number of seats is restricted to 25 only.
- No Registration fees.
- Selection on first come first served basis.
- Preference will be given to the young faculty based on eligibility criteria.
- Selected candidates will be intimated by e-mail only.
- Send the duly filled in Registration forms to the Co-ordinator(s) **by post/mail on or before 7th November 2018**

TWO WEEKS FDTP on “LINEAR INTEGRATED CIRCUITS EC8453 (R-2017)”

12th – 24th, November 2018

REGISTRATION FORM

Co-ordinators
Dr.S.Thayammal,Ph.D.,
Associate Professor & Head-ECE
Mrs.R.Rajalakshmi,M.Tech.,(Ph.D).,
Assistant Professor-ECE



Approved by
ANNA UNIVERSITY
CHENNAI-25



Organized By
**KALASALINGAM INSTITUTE OF
TECHNOLOGY**
Department of ECE
Anand Nagar, Krishnankoil-626126
www.kit-edu.in
kitprl@yahoo.com

**Two Weeks FDTP on
Linear Integrated Circuits (EC8453)
12th – 24th, November 2018**

REGISTRATION FORM

1. Name:
2. Designation:
3. Qualification:
4. Age & Gender:
5. Professional Experience (in Years)
Teaching:
Industry:
6. Did you attend this course earlier? Yes/No
7. Address of the College/Institution:
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8. Research Interest:
9. Phone (Mobile):
10. E-Mail:
11. Accommodation Required? Yes/No

DECLARATION

The information provided by me is true to the best of my knowledge. I agree to abide by the rules and regulations governing by the FDTP. If selected I shall attend the program for the entire duration.

Signature of the Participant

Date:
Place:

SPONSORSHIP

Dr./Mr./Mrs. _____

is an employee of our institution. He / She will be permitted for the above FDTP, if selected.

Signature of the Principal with Seal

For further details, contact:
Dr.S.Thayammal,Ph.D.,
Mrs.R.Rajalakshmi,M.Tech.,(Ph.D).,

The Co-ordinators,
FDTP on
" **Linear Integrated Circuits** ",
Department of ECE,
Kalasalingam Institute of Technology,
Anand Nagar,
Krishnankoil – 626 126

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ORGANIZING COMMITTEE

CHIEF PATRON: Dr.M.K.Surappa
Vice-Chancellor,
Anna University,
Chennai

PATRON: Dr.J.Kumar
Registrar,
Anna University,
Chennai

CHAIR: Dr.D.Sridharan
Director,CFD

Dr.I.Arul Aram
Addl. Director, CFD

CO CHAIR: Dr.S.Harikrishnan, M.E.,Ph.D.,
Principal, KIT
Dr.S.Thayammal,Ph.D.,
HOD/ECE, KIT

CO-ORDINATORS:
Dr.S.Thayammal
ASP & Head/ECE
R.Rajalakshmi-AP/ECE

VENUE: 4th Floor-Seminar Hall,
Dept of ECE, KIT

IMPORTANT DATES:
Submission of Application : **07.11.2018**
Intimation of Selection : **08.11.2018**
Confirmation by Participants : **09.11.2018**
Date of FDTP: 12.11.2018 to 24.11.2018