

ABOUT THE INSTITUTION

The Kalasalingam Institute of Technology (KIT) was established in the year 2008 by our **Chairman late Kalvivalal Thiru. T.Kalasalingam**, run by “Kalasalingam and Anandam Ammal Charities”. Now, this esteemed institution is functioning under leadership of **Ilayavallal Dr.K.Sridharan,MBA.,Ph.D., Secretary, Dr.S.ShasiAnand,Ph.D., Director, Mr.S.Arjun Kalasalingam,MS., Director and Dr.S.Harikrishnan, M.E.,Ph.D.,Principal.**

KIT is located in a sprawling area of 25 acres in Anand nagar, Krishnankoil. The Institution is approved by Govt. of TamilNadu and AICTE, New Delhi. It is affiliated to Anna University, Chennai. The college is offering the UG courses such as **ECE, CIVIL, EEE, CSE, IT** and PG courses such as **M.E in VLSI & CSE**. Our college is one of the **NPTEL Nodal center** which provides video lectures and power point presentations for nearly 850 courses to enhance the teaching & learning process. The Institution has strong Industrial Interaction Cell and 25 faculty numbers are pursuing Ph.D programme from different domain in various Universities. Our recruiters are Infosys, Wipro, HCL, Infoview, Vernalis, Tessolve, Zoho, Sutherland, etc

ABOUT THE DEPARTMENT

The department of ECE is offering **B.E in ECE** programme with an intake of **120 seats** and **M.E in VLSI Design** programme. Since the Kalasalingam Institute of Technology was established in the year 2008, with a view to offer quality in higher education to numerous young fellows. The programme follows the course curriculum given by Anna University, Chennai.

ABOUT THE FDTP

The objective of the FDTP is to bring together experts from Industries, Academic and other user communities on a common platform to discuss and share their expertise and ideas about the technologies & recent trends in the field of Embedded & Real Time Systems (ERTS).

COURSE COVERAGE

The main focus will be on Anna University (Regulation 2013), B.E-ECE 7th semester syllabus of EC6703 – ERTS.

The following topics will be covered:

- Embedded system design process
- ARM Processor
- Embedded Computing Platform Design
- Compilation techniques
- Processes & Operating Systems
- System Design Techniques & Networks
- Quality Assurance techniques
- Embedded related Case Studies

ABOUT REGISTRATION

- Number of seats is restricted to 25 only.
- No Registration fees.
- Selection on first come first served basis.
- Preference will be given to the young faculty based on eligibility criteria.
- Selected candidates will be intimated by e-mail only.
- Send the duly filled in Registration forms to the Co-ordinator(s) **by post on or May 8th May 2017.**

SEVEN DAYS FDTP on “Embedded & Real Time Systems EC6703 (R-2013)”

15th – 22nd, May 2017

REGISTRATION FORM

Co-ordinators
Mrs.R.Rajalakshmi,M.Tech.,(Ph.D).,
Ms.B.Hema,M.E.,
Assistant Professors of ECE, KIT



Approved by
ANNA UNIVERSITY
CHENNAI-25



Organized By
**KALASALINGAM INSTITUTE OF
TECHNOLOGY**
Department of ECE
Anand Nagar, Krishnankoil-626126
www.kit-edu.in
kitprl@yahoo.com

**Seven Days FDTP on
Embedded & Real Time Systems
(EC6703)
15th – 22nd, May 2017
REGISTRATION FORM**

1. Name:
2. Designation:
3. Qualification:
4. Age & Gender:
5. Professional Experience (in Years)
- Teaching:
- Industry:
6. Did you attend this course earlier? Yes/No
7. Address of the College/Institution:
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.....
8. Research Interest:
9. Phone (Mobile):
10. E-Mail:
11. Accommodation Required? Yes/No

DECLARATION

The information provided by me is true to the best of my knowledge. I agree to abide by the rules and regulations governing by the FDTP. If selected I shall attend the program for the entire duration.

Signature of the Participant

Date:
Place:

SPONSORSHIP

Dr./Mr./Mrs. _____

is an employee of our institution. He / She will be permitted for the above FDTP, if selected.

Signature of the Principal with Seal

For further details, contact:
Mrs.R.Rajalakshmi, M.Tech.,(Ph.D),
Ms.B.Hema, M.E.,
The Co-ordinators,
FDTP on
" Embedded & Real Time Systems ",
Department of ECE,
Kalasalingam Institute of Technology,
Anand Nagar,
Krishnankoil – 626 126

Phone : 7502638715, 9750682103
Email: rajeemtech@gmail.com
hema.shivam@gmail.com

ORGANIZING COMMITTEE

CHIEF PATRON:

Vice-Chancellor
Anna University
Chennai

PATRON:

Dr.S.Ganesan
Registrar
Anna University
Chennai

CHAIR:

Dr.K.Shanthi
Director,CFD
Dr.D.Sridharan
Addl. Director, CFD

CO CHAIR:

Dr.S.Harikrishnan, M.E.,Ph.D.,
Principal, KIT
Mr.S.Vairaprakash, M.E.,(Ph.D),
HOD/ECE, KIT

CO-ORDINATORS:

Mrs.R.Rajalakshmi, M.Tech., (Ph.D),
Ms.B.Hema, M.E.,
Assistant Professors of ECE, KIT.

**VENUE: 3rd Floor-Seminar Hall,
Dept of ECE, KIT**

IMPORTANT DATES:

Submission of Application : **08.05.2017**
Intimation of Selection : **09.05.2017**
Confirmation by Participants : **10.05.2017**

Date of FDTP: 15.05.2017 to 22.05.2017